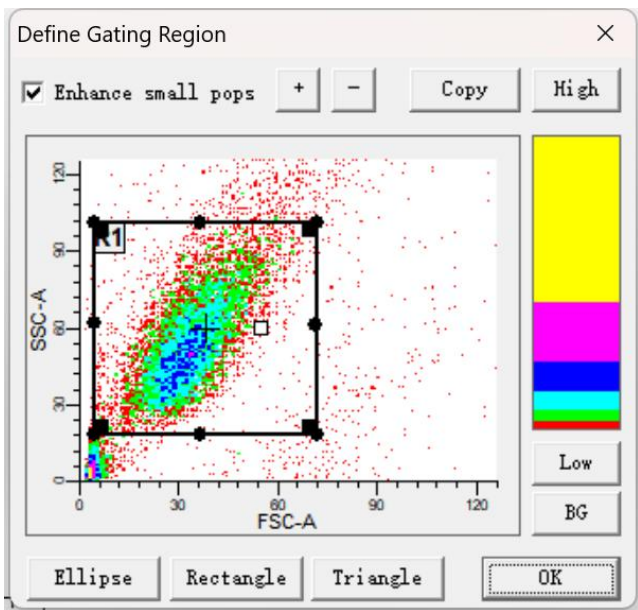
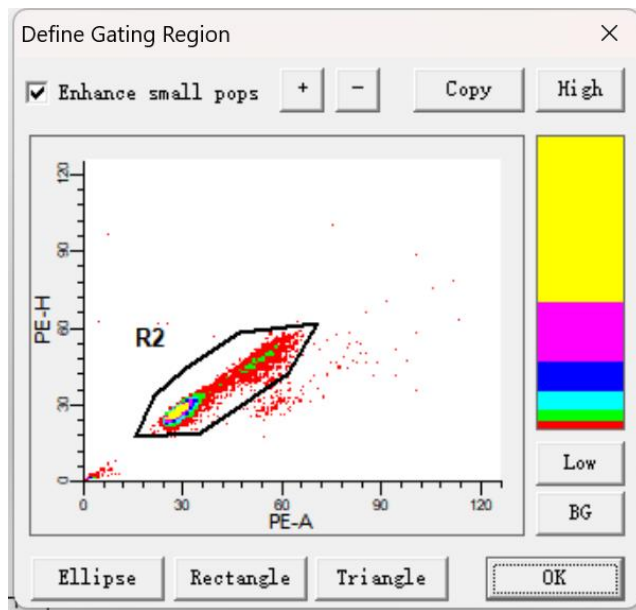


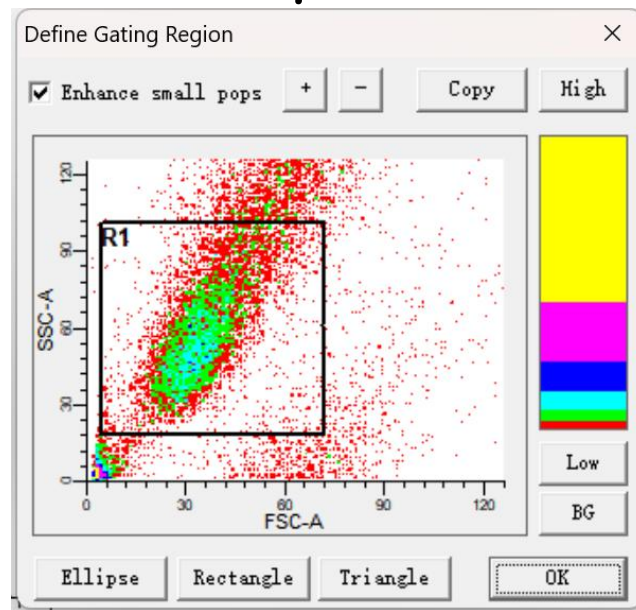
### A549 DMSO Gate1



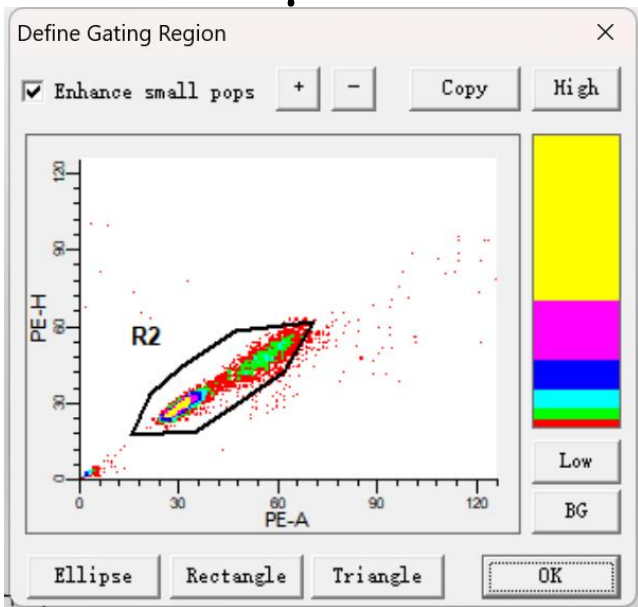
### A549 DMSO Gate2



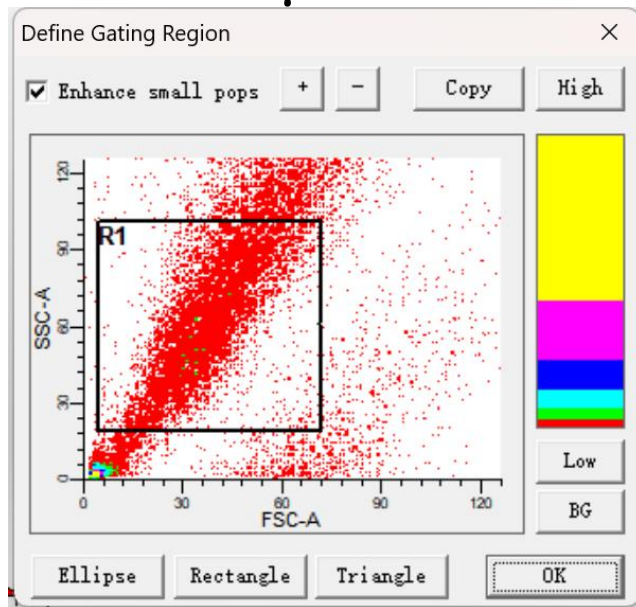
### A549 2μM Gate1



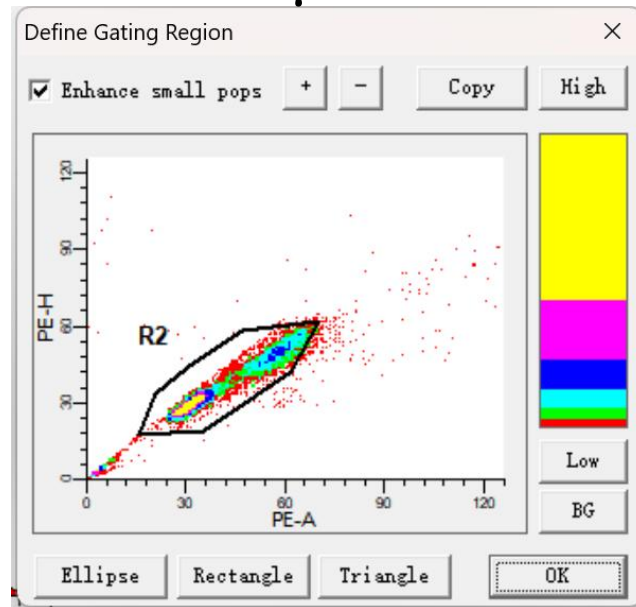
### A549 2μM Gate2



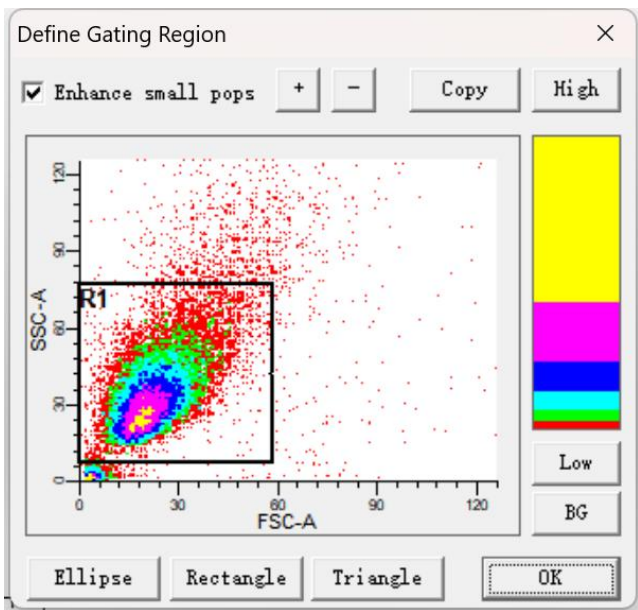
### A549 4μM Gate1



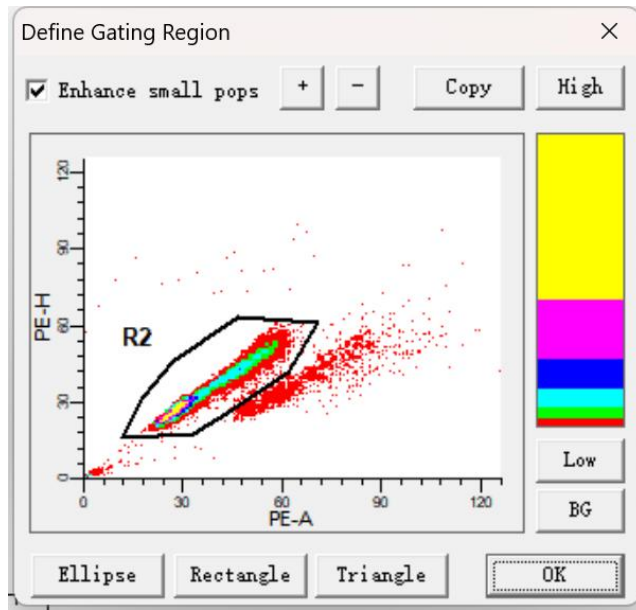
### A549 4μM Gate2



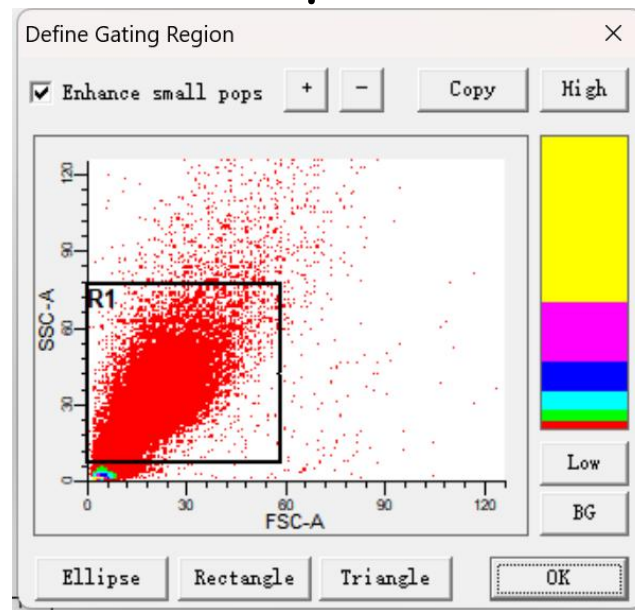
### H460 DMSO Gate1



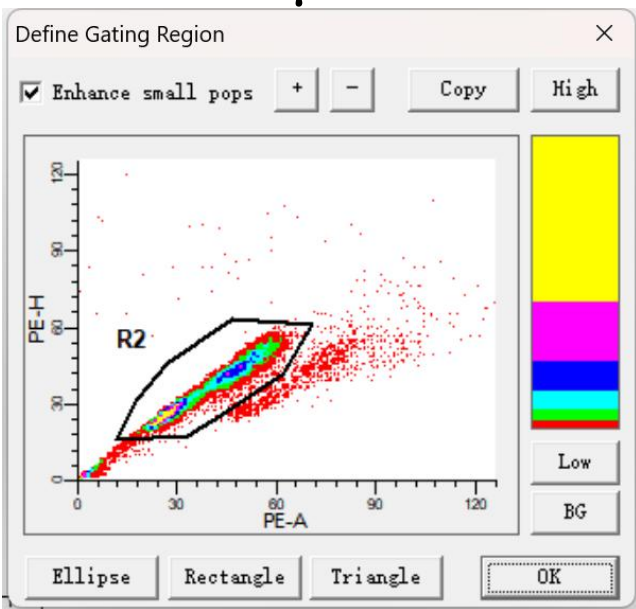
### H460 DMSO Gate2



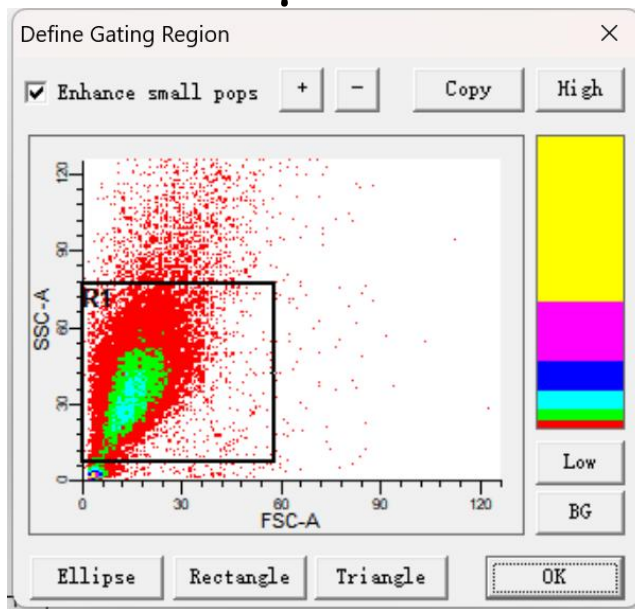
### H460 2 $\mu$ M Gate1



### H460 2 $\mu$ M Gate2



### H460 4 $\mu$ M Gate1



### H460 4 $\mu$ M Gate2

